

CURRICULUM VITAE

Pradnya Paresh Sutar

B-302, 'Adinath Alpine',
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Career Objective

Looking forward to build my career by utilizing all opportunities to implement my skills and knowledge in order to pursue a long term relationship with the organization.

Career Summary

- M.E. in Electronics & Telecommunication Engineering from Terna Engineering College, Nerul (Mumbai University) in January,2018
- AMIETE(ET) in Electronics & Telecommunication Engineering from IETE,New Delhi in February ,2015
- Completed AMIETE(ET), while doing job of Laboratory Technician in Electronics and Telecommunication Engg. Department of Gharda Institute of Technology, Lavel, Tal. Khed,Dist.Ratnagiri(Mumbai University)
- Advanced Diploma in Computer Software System Analysis and Application from Prin. L.N. Welingkar Institute of Management Development & Research, Sion(Now in Matunga) in June 1996
- Diploma in Electronics and Communication Engineering from Government Polytechnic, Ratnagiri in May 1995
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Personal and Technical Skills

- Good oral and written Communication
- Good Computer and Internet capabilities
- Ability to work independently as well as in a team
- Quick Learner, Confident, Punctual & Hard worker
- Efficient in handling laboratories of Electronics and Telecommunication Engineering Department and having skill in giving practical knowledge
- Skill in handling Antenna Trainer,Optical Fiber Trainer,Microwave Trainer,Satellites Communication Trainer Kits,Spectrum Analyzer, Vector Network Analyzer,Radar,Analog communication Trainer kits,Microprocessor and Microcontroller Trainer kits
- Skill in Handling Electronics equipments like CROs,Digital Storage Oscilloscopes,Function Generatos ,in designs of electronic circuits of Anlog and Digital type on bread boards and in student's projects
- Handled softwares like OPTIFDTD, OPTIBPM,TINA, Multisim, Matlab, TINA, Microwind, Proteus

Work Experience

- Laboratory Technician at Gharda Institute of Technology, Lavel, Tal. Khed, Dist.Ratnagiri (July 2008-April 2015) (Mumbai University)
- Computer Institutional Faculty at Infotech Institute,Tal.Chiplun,Dist.Ratnagiri (August 1997 - April 1998)
- Computer Institutional Faculty at Megabyte Institute,Tal.Dist.Ratnagiri (July 1996-March 1997)

Key Responsibilities Handled

- Involved in the development of new Laboratories of Electronics & Telecomm. Engg. Department from starting of the college of Gharda Institute of Technology,Lavel
- Efficiently handled laboratories of Electronics Devices & Circuits I & II, Analog Communication Engineering, Antenna & Radio Wave Propagation , Circuits & Networks, Optical Communication, Microwave Engg., Microprocessor, Microcontroller & Radar Engineering
- Worked in installation, troubleshooting and maintenance of electronic circuits and devices.
- Handled all responsibilities related to laboratory.
- Worked with eager to learn the new trends and techniques of electronic methodologies.
- Efficiently worked in all laboratories as skilled in Circuit implementation
- Worked in various activities of E & TC Engineering department including department's Student Association activities, Workshops, Trainings ,department's sessional Exams ,Technical Festivals,Cultural activities and other department's day to day work
- Worked in Exam Section(in CAP as well as in conduction of university exams) from 2008-2014

Academia

Exam.	Institute	Board	Year	Grade point / Percentage
M.E. (E & TC Engg.)	Terna College of Engineerig,Nerul	Mumbai University	January,2018	8.01/10 CGPI
AMIETE ET (E & TC Engg.)	IETE,New Delhi	IETE,New Delhi	February, 2015	6.92/10 CGPA
Advanced Diploma in Computer Software System Analysis and Application	Prin.L.N.Welingkar Institute of Mgmt.Devlp.& Research,Sion (Now in Matunga)	Board Of Technical Examinations, Maharashtra	June,1996	64.73%
Diploma in Electronics & Communication Engg.	Govt. Polytechnic Ratnagiri	Board Of Technical Examinations, Maharashtra.	May 1995	67%
S.S.C.	R.B.Shirke, Highschool, Ratnagiri	Maharashtra State Board of Secondary & HSE(Kolhapur Div.)	March 1992	83.85%

Elective Subjects Studied

Post Graduate

- Sem.I 1. Advanced Satellite Communication
2. Micro Electro Mechanical Systems
- Sem.II 1. Advanced Antenna and Arrays
2. Network Security

Under Graduate(AMIETE)

- Data Communication & Computer Networks
- Radar & Navigational Aids

Academic Project Undertaken

Post Graduate

- Mach Zehnder Interferometer Based Optical True Time Delay Line

Under Graduate(AMIETE)

- GSM based Data Logger

Diploma

- Electronics Horn for Car

Seminar

Post Graduate

- True Time Delay Line for Antenna Beam forming

Under Graduate(AMIETE)

- IEEE 802.15.3c-Wireless Standard giving data rates above 1Gbps

Publications

A) International Journals :

- [1] Pradnya Sutar, Dr. Jyothi Digge, "True Time Delay Line for Antenna Beam forming", International Journal of Science and Research(IJSR), ISSN(Online):2319-7064, Volume 6, Issue 3, pp. 926-935, Index Copernicus Value (2015): 78.96 | Impact Factor (2015): 6.391, March, 2017(UGC approved International Journal)
- [2] Pradnya Sutar, Dr. Jyothi Digge, "Mach Zehnder Interferometer True Time Delay Line", International Journal of Engineering Technology Science and Research(IJETSR), ISSN 2394-3386, Volume 4, Issue 4, pp.211-215, Impact Factor :2.120, April, 2017(UGC approved International Journal)
- [3] Pradnya Sutar, Dr. Jyothi Digge, "Nine bit Mach Zehnder Interferometer based Comb Shaped Optical True Time Delay Line for Antenna Beam forming", Asian journal of convergence in Technology, Impact factor:2.71, I2CT, 2018(UGC approved International Journal)

B) International Conferences :

- [1] Pradnya Sutar, Dr. Jyothi Digge, "Mach Zehnder Interferometer True Time Delay Line", International Conference on Advanced Trends Engineering(ICATE-2017) by ISTE, in conference proceeding having ISBN.978-93-86171-02-3, April, 2017
- [2] Pradnya Sutar, Dr. Jyothi Digge, "Design and Simulation of Optical waveguides on different materials", 3rd International Conference on Engineering Confluence (Equinox 2017), pp.1-5, September 2017
- [3] Pradnya Sutar, Dr. Jyothi Digge, "Nine bit Mach Zehnder Interferometer based Comb Shaped Optical True Time Delay Line for Antenna Beam forming" IEEE's 3rd International Conference for Convergence in Technology (I2CT), Pune, 2018

Professional Membership

- Life member, Institution of Electronics and Telecommunication Engineers(IETE),New Delhi

Workshops attended

- Workshop on GPS in E&TC Dept., GIT, Lavel on 18th March 2011
- Analog Electronics workshop by IIT Kharagpur in GIT Lavel from 4th to 14th June 2013
- Workshop on Network Security organised by Dept,of Information Technology at Terna Engineering college,Nerul in 5th to 10th March 2016

Personal Details

Nationality : Indian
Date of Birth : 19 September 1977
Gender : Female
Marital Status : Married
Languages Known : English,Hindi,Marathi
Address : Mrs.Pradnya Paresh Sutar
: B-302, 'Adinath Alpine',
: Sector 25,Plot No.28A,
: Kamothe,410 209

I hereby declare that the above information given by me is correct to the best of my knowledge and belief.

Date : 23/02/2026

Place : Kamothe

(Mrs. Pradnya Paresh Sutar)