



Vrishali Arun Walanj

ME(EXTC)

Assistant Professor

Email: vrishaliwalanj@ternaengg.ac.in

Address for Correspondence:

C401 gajanan maharaj nagar

Beturkar pada kalyan west

Professional Qualification:

- **Master of EXTC, 2016**
Mumbai University, Maharashtra
- **Bachelor of Electronics Engineering, 2013**
Mumbai University, Maharashtra

Short Biodata: I am Vrishali Walanj working as Assistant Professor in Terna Engineering College from last 4 years.

Major Subjects:

1. Application of integrated circuits
2. Electrical circuits and machines
3. Instrumentation control
4. Communication system
5. Digital communication
6. Micro-electro mechanical system

List of Publication:

Journal Publications:

1. Detection of brain tumor using MRI Image, International Journal of Infinite Innovations in Technology|ISSN:2278-9057 IJIIT|Volume-IV|Issue-II|2015-2016 October|Paper-03 Reg. No.:20150903|DOI:V4I2P03
2. Detection of brain tumor using MRI Image, International Journal of Infinite Innovations in Technology|ISSN:2278-9057 IJIIT|Volume-IV|Issue-I|2015-2016 July|Paper-08 Reg. No.:20150608|DOI:V4I1P08
3. Data Package Handover Characteristics in Heterogeneous Network for Vehicular Communication: A Short Review,

Conferences and Workshops Publications:

1. Detection Of Brain Tumor Using Mri Image, International Journal of Engineering Research in Electronic and Communication Engineering (IJERECE) Vol 2, Issue 11, November 2015

Software Tools:

Matlab, Automation studio, QUCS, Proteus, MCU8051 IDE

Experience:

- 4years 3 months in Terna Engineering College
- 1 year in BNN College

Responsibilities Handled:

- MARSA coordinator
- Counselling team member
- Cultural member
- Class Advisor
- Mini project coordinator
- INC lab in charge
- Gate coaching under Prerana Scheme

Declaration

I hereby declare that the foregoing information are correct and complete to the best of my knowledge.

Vrishali Arun Walanj

Place: Navi Mumbai