Virendra Ramesh Koli

Assistant Professor

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Address for Correspondence:

Flat No. 303, Mrunali heights CHS Ltd., Belavali, Badlapur, Thane-410210

Professional Qualification:

➤ Master of Technology VLSI Technology, 2012

Department of Electronics North Maharashtra University Jalgaon Maharashtra

> Bachelor of Electronics and Telecommunication Engineering, 2009

Shree Sant Gajanan Maharaj College of Engineering, Shegaon, (Shree Sant Gadge Baba Amravati University, Amravati, Maharashtra)

Patents/ Products/ Consultancy: 05 Patents

Products Developed: No

Consultancy Projects: No

Reviewer:

Science Publications (Home | Science Publications (thescipub.com))

Major Subjects:

- Big Data Analytics
- ➤ Cloud Computing and Security
- ➤ Digital VLSI design

List of Publications:

 $1.\,BLOOD$ BANKING VIA CLOUD COMPUTING USING MACHINE LEARNING TRACKING THE BLOOD DONOR LOCATION AT REAL-TIME

VR Koli, S Garg, SK Choudhary - Harbin Gongye Daxue Xuebao/Journal of Harbin, 2022

1. Journal Publications: Live Reporting and Monitoring of Intelligence Infant Disease Prediction V Koli, S Garg, SK Choudhary - Computer Integrated Manufacturing Systems, 2022



STTP & Workshops Attended

- 1. STTP Attended "Embedded System & Real Time Programming" July 11-15, 2011, TEC, Nerul.
- 2. STTP Attended "Wireless Communication" July 2012, TEC, Nerul.
- 3. One-day Workshop on "Wireless Networks" at Terna Engineering College in association with TeleNetworks Technologies on 21 Sept. 2017.
- 4. Two days workshop on "Effective Research Methodology" from 5th January 2018 to 6th January 2018 at Terna Engineering College, Nerul, Navi Mumbai
- 5. One-day Workshop on "Intellectual Property Rights" on 4th August 2018 at Terna Engineering College, Nerul, Navi Mumbai.
- 6. Participated in the Inauguration function of Lotfi Zadeh Center of Excellence Health Sciences and Technology on Sept. 26, 2018, organized by Terna Research Society, Terna Campus, Nerul, Navi Mumbai.
- 7. A one-day workshop on "MATLAB simulation" on 3rd January 2019, EXTC department at Terna Engineering College, Nerul, Navi Mumbai.
- 8. Four days workshop on "Network Associate" at Terna Engineering College in association with TeleNetworks Technologies on 07-10 Feb.2019.
- 9. A five-day workshop on "Cyber Security and Laws" at Terna Engineering College, association with TeleNetworks Technologies on Sept. 13-17, 2019.
- 10. Participated in a one-week online FDP on "Python 3.4.3" by Spoken Tutorial Project, IIT, Bombay from 25th April 30th April 2020 remotely at Sinhagad Institute of Technology, Lonavala.
- 11. Passed an online "Python 3.4.3" exam conducted remotely at IIT, Bombay (Spoken Tutorial) on 08th May 2020.
- 12. Participated in online one-week FDP on "Research Funding Projects and IPR" organized by K. C. College of Engineering and Management Studies and Research, Thane, from 07th May 10th May 2020.
- 13. Attended a one-week workshop on "Cyber Security Awareness" organized by the Department of Electronics and Telecommunication Engineering, Terna Engineering College, Nerul from 07-11 Oct 2022 association with Tele Networks Technology Private Limited.

Online Webinars Attended:

1. Attended online "Turnitin online demonstration" organized by Terna Engineering College, Nerul dated

20 April 2020.

2. Attended Webinar on "Unlimited Mind Power" on 16th May 2020, organized by the Department of

Electronics, SNDT Woman University, Premlila Vithaldas Polytechnic.

3. Attended Webinar on "Future Effective Learning Techniques" on 21st May 2020, organized by Sinhgad

Institute of Technology, Lonavala, Pune.

4. Attended Four days Webinar series on "Research, Technology Trends in Digital Future and Media

Responsibility during Pandemic" on 17th - 24th May 2020, organized by Dr. Vithalrao Vikhe Patil College

of Engineering, Ahmednagar.

5. Attended Two days Webinar series on "Recent Trends in Microwave Engineering and Internet of Things"

held on $06^{th} - 7^{th}$ June 2020, organized by D. J. Sanghvi College of Engineering, Mumbai.

6. Attended online International Webinar on "Photonics Crystal Fiber and its Application" on 29th June 2020,

organized by EXTC, ELX and Optiwave Canada, Terna Engineering College, Nerul.

Funding Received: 40000/-

Name of Funding Organization: University of Mumbai (Minor Research)

Amount Awarded: 40000/-

Period of Grant Award: 12 Months (2018-19)

Title of Project: Smart Healthcare System.

Role on Project: Principal Investigator

Software Tools:

1. Xilinx

2. MATLAB

3. Python

4. SQL, No-SQL

Professional Association:

➤ Institute of Electrical and Electronics Engineers (IEEE)-Member

> Indian Society of Technical Education (ISTE) - Life Member

Experience:

➤ 10.6 years, the University of Mumbai Approved Teaching Experience at Terna Engineering College, Nerul Navi Mumbai

1st June 2013- 31st Jan 2022: Assistance Professor (Grade Pay 6000/-)

1st Feb 2022- till date: Assistance Professor (Grade Pay 7000/-)

Responsibilities Handled:

- ➤ Member Anti-Ragging committee since 2015.
- ➤ Teaching Assistance "Control System" Workshop Conducted by IITKP in 2015.
- ➤ Teaching Assistance "Signals System" Workshop Conducted by IITKP in 2013.
- ➤ Coordinator Avalon-2017 National Level Technical Fest- 2017.
- ➤ Publicity Committee Coordinator (Equinox-2017,2018,2019,2021) National Level Conference, TEC, Nerul, Navi Mumbai
- ➤ Committee Member Various STTP in TEC, Nerul
- ➤ NBA and NAAC Departmental Coordinator since 2017
- ➤ University, TE (EXTC) Online Exam Co-Ordinator AY-2020.
- University, DSE (EXTC) Online Exam Co-Ordinator AY-2020.
- University, ME (EXTC) Online Exam Co-Ordinator AY-2020
- ➤ University, TE (EXTC) Online Exam Co-Ordinator AY-2021.
- ➤ University, DSE (EXTC) Online Exam Co-Ordinator AY-2021.
- ➤ University, ME (EXTC) Online Exam Co-Ordinator AY-2021.
- Worked as CO PO and PSO Feedback Coordinator Since 2017.
- ➤ Optical Communication and Microwave Lab in charge since 2017.
- Worked as Co-Academic Program Coordinator A.Y. 2015.
- Worked as Exam Coordinator A. Y. 2016-2017.

Talks/Guest Lectures Delivered:

Invited Lecture on "VLSI Design" at Padmabhushan Vasantdada Patil Pratishthan's College of Engineering Mumbai.

SESSION CHAIR/JUDGE:

- 1. Judge Avalon-2018 National Level Technical Fest- 2018, TEC, Nerul
- 2. Judge Technical paper presentation, Avalon-2018 National Level Technical Fest- 2018, TEC, Nerul

Declaration

I hereby declare that the foregoing information are correct and complete to the best of my knowledge.

Virendra Ramesh Koli

Place: Navi Mumbai